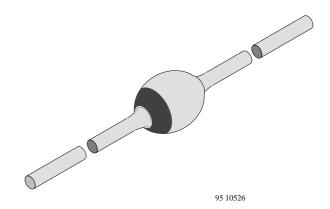


# **Fast Soft Recovery Rectifier**

#### **Features**

- Miniature axial leaded
- Glass passivated
- Hermetically sealed glass envelope
- Low reverse current
- High reverse voltage



#### Applications

TV and monitor SMPS Electronic ballast

### **Absolute Maximum Ratings**

T<sub>i</sub> = 25°C

Parameter	Test Conditions	Туре	Symbol	Value	Unit
Reverse voltage=		BYT42A	V <sub>R</sub> =	50	V
Repetitive peak reverse voltage		BYT42B	V <sub>RRM</sub>	100	
		BYT42D		200	
		BYT42G		400	
		BYT42J		600	
		BYT42K		800	
		BYT42M		1000	
Peak forward surge current	tp=8.3 ms, half sinewave		I <sub>FSM</sub>	30	А
Average forward current	Lead length I = 10 mm, $T_L = 25^{\circ}C$		I <sub>FAV</sub>	1.25	A
Junction and storage temperature range			T <sub>j</sub> =T <sub>stg</sub>	-55+175	°C
Non repetitive reverse avalanche energy	I <sub>(BR)R</sub> =0.4A		E <sub>R</sub>	10	mJ

#### **Maximum Thermal Resistance**

#### $T_i = 25^{\circ}C$

Parameter	Test Conditions	Symbol	Value	Unit
Junction ambient	Lead length I = 10 mm, T <sub>L</sub> = constant	R <sub>thJA</sub>	60	K/W
	on PC board with spacing 25mm		110	Í

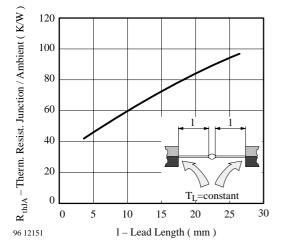


#### **Electrical Characteristics**

 $T_j = 25^{\circ}C$ 

Parameter	Test Conditions	Туре	Symbol	Min	Тур	Max	Unit
Forward voltage	I <sub>F</sub> =1A		V <sub>F</sub>			1.4	V
Reverse current	V <sub>R</sub> =V <sub>RRM</sub>		1_			5	μA
	V <sub>R</sub> =V <sub>RRM</sub> , T <sub>j</sub> =150°C		I <sub>R</sub>			150	
Reverse breakdown voltage	I <sub>R</sub> =100μΑ	BYT42A	V <sub>(BR)R</sub>	50			V
		BYT42B		100			
		BYT42D		200			
		BYT42G		400			
		BYT42J		600			
		BYT42K		800			
		BYT42M		1000			
Reverse recovery time	I <sub>F</sub> =0.5A, I <sub>R</sub> =1A, i <sub>R</sub> =0.25A	BYT42A-	- t <sub>rr</sub>			150	
		BYT42J				130	ns
		BYT42K-				200	
		BYT42M					

## **Characteristics** ( $T_j = 25^{\circ}C$ unless otherwise specified)





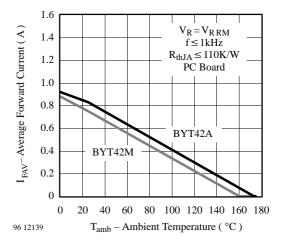
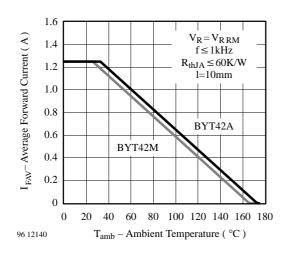


Figure 2. Max. Average Forward Current vs. Ambient Temperature





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Figure 3. Max. Average Forward Current vs. Ambient Temperature

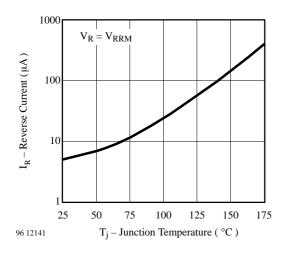


Figure 4. Max. Reverse Current vs. Junction Temperature

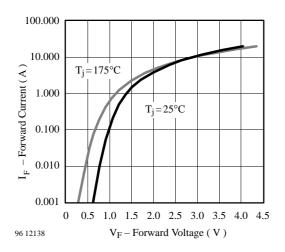


Figure 5. Max. Forward Current vs. Forward Voltage

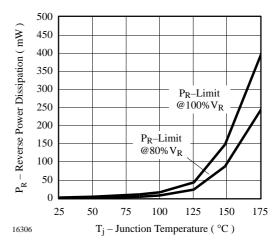


Figure 6. Max. Reverse Power Dissipation vs. Junction Temperature

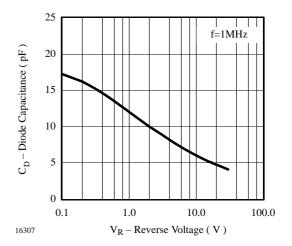
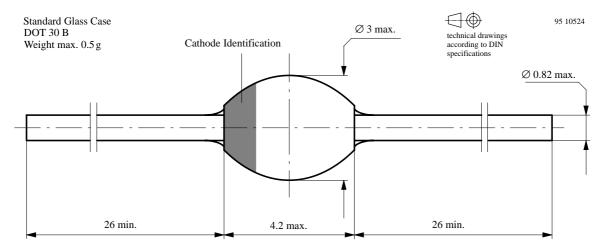


Figure 7. Diode Capacitance vs. Reverse Voltage



#### **Dimensions in mm**





#### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.

2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay-Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay-Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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